Ghozeil et al.

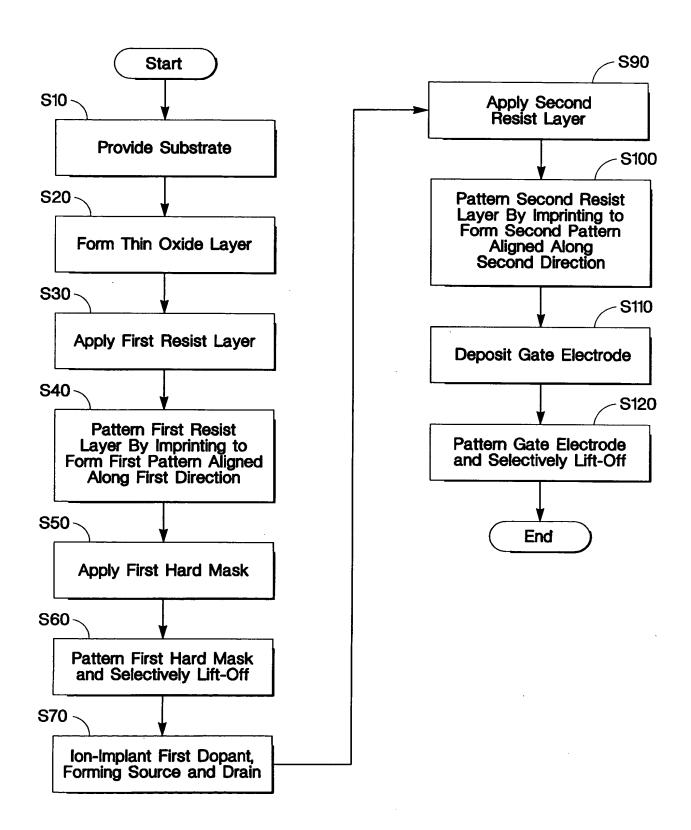


Fig. 1

# ARRAYS OF NANOSCOPIC MOSFET TRANSISTORS AND FABRICATION METHODS Ghozell et al.

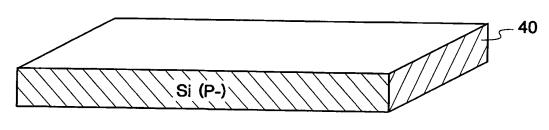


Fig. 2

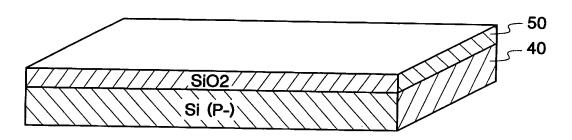


Fig. 3

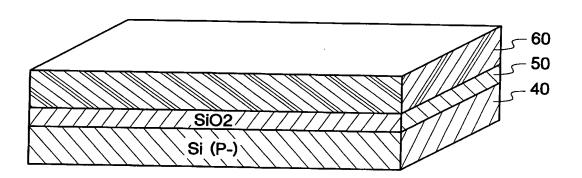


Fig. 4

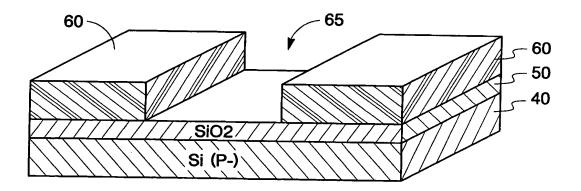


Fig. 5

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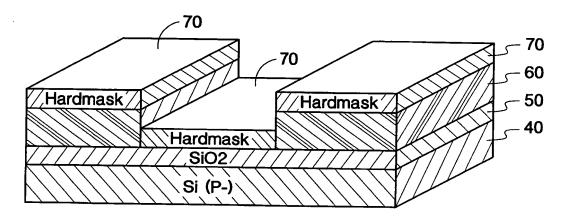


Fig. 6

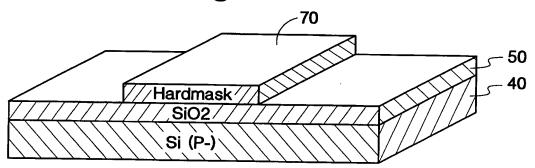


Fig. 7

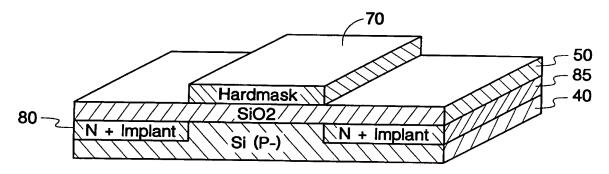


Fig. 8

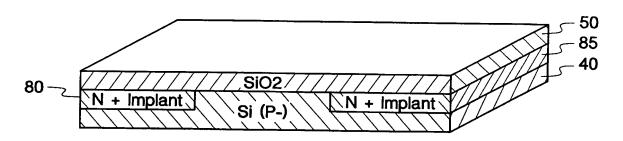


Fig. 9

## ARRAYS OF NANOSCOPIC MOSFET TRANSISTORS AND FABRICATION METHODS Ghozeil et al.

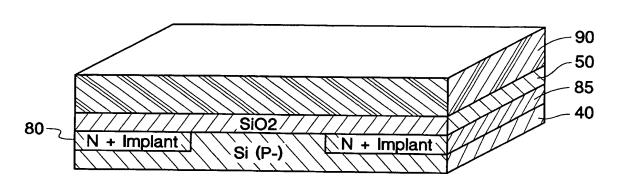


Fig. 10

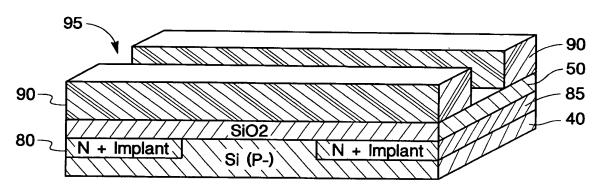


Fig. 11

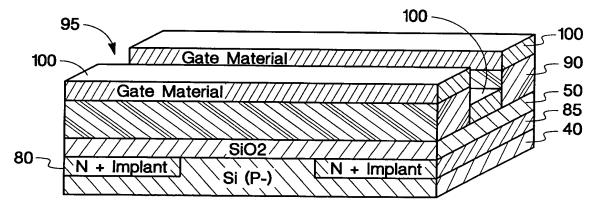


Fig. 12

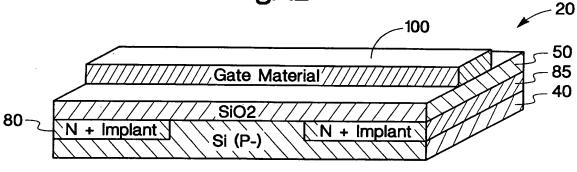
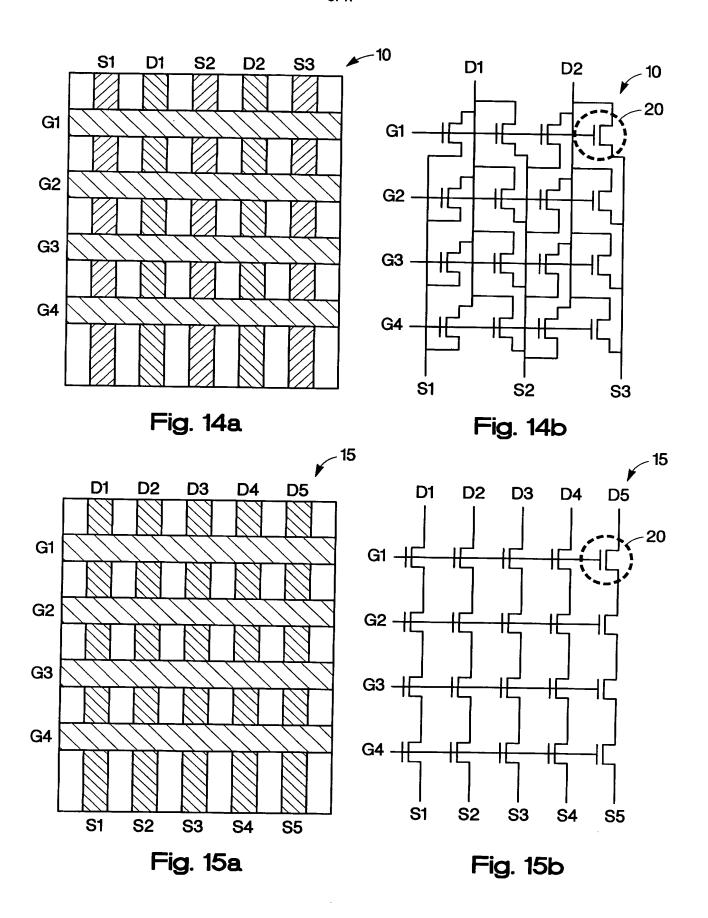


Fig. 13

# ARRAYS OF NANOSCOPIC MOSFET TRANSISTORS AND FABRICATION METHODS Ghozeil et al.



## ARRAYS OF NANOSCOPIC MOSFET TRANSISTORS AND FABRICATION METHODS Ghozell et al.

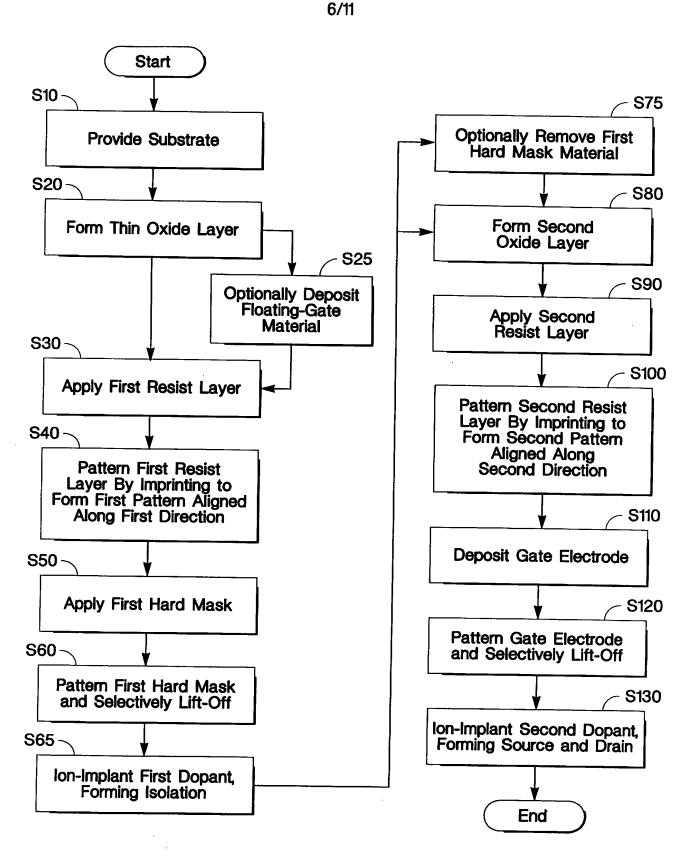


Fig. 16

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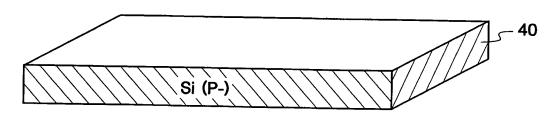


Fig. 17

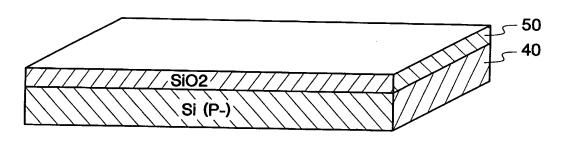


Fig. 18

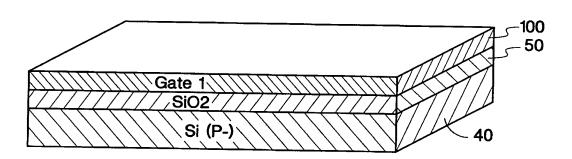


Fig. 19

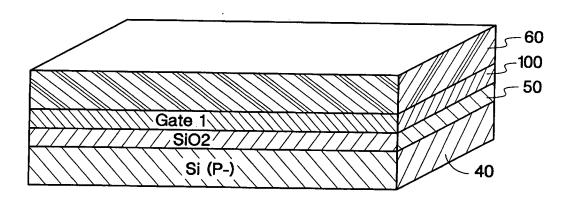


Fig. 20

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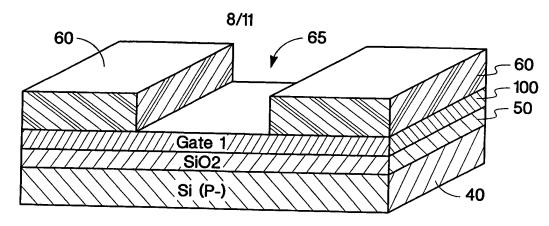


Fig. 21

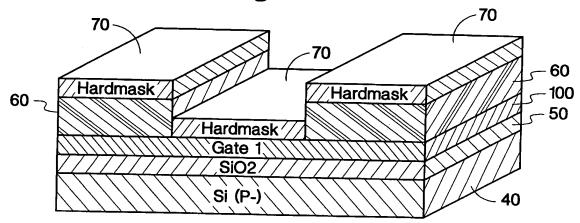


Fig. 22

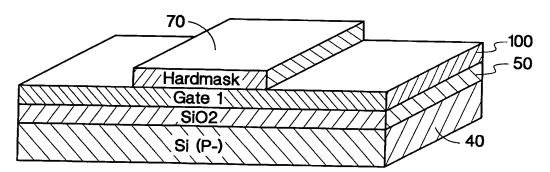


Fig. 23

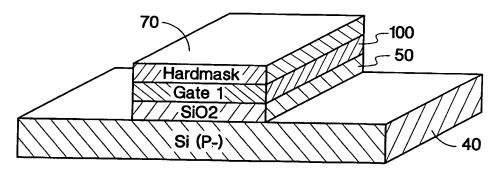


Fig. 24

Ghozeil et al.

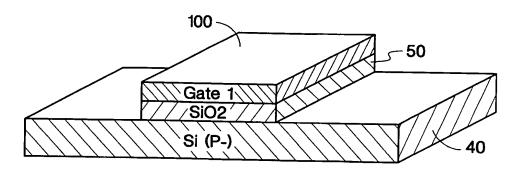


Fig. 25

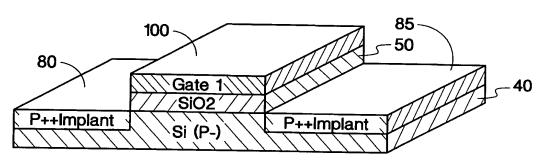


Fig. 26

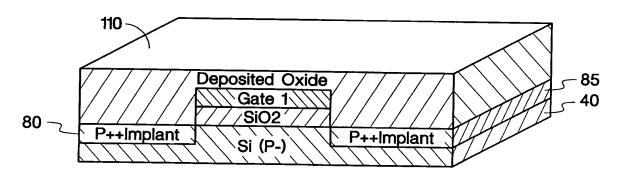


Fig. 27

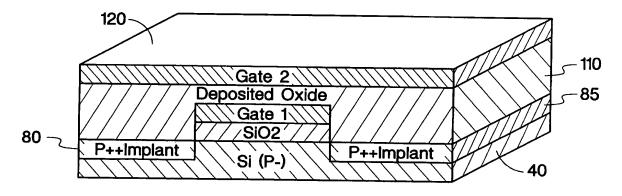


Fig. 28

Ghozeil et al.

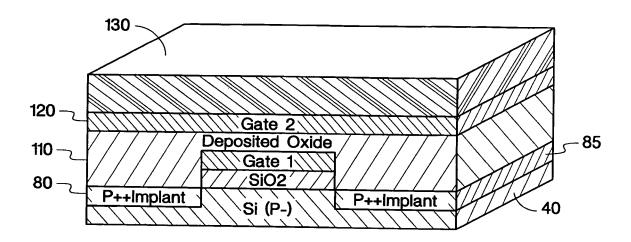


Fig. 29

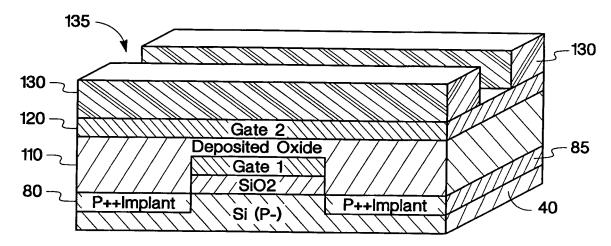


Fig. 30

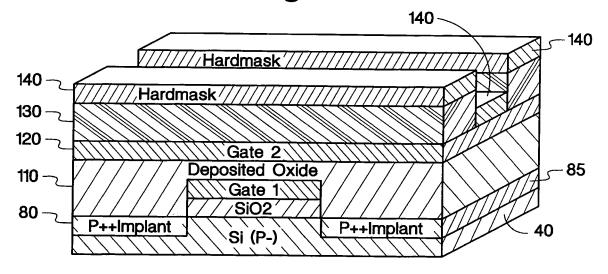


Fig. 31

Ghozeil et al.

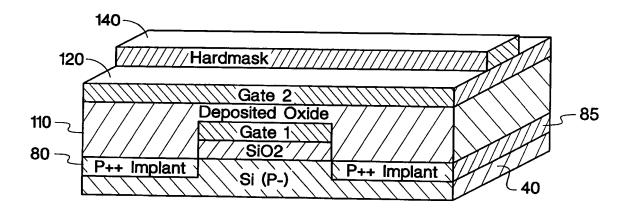


Fig. 32

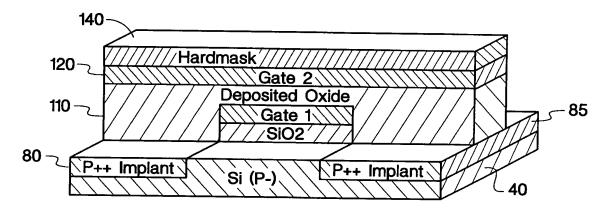


Fig. 33

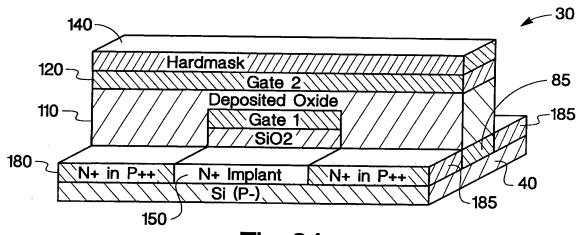


Fig. 34